

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2797	isolation adj cell	US-PGPUB; USPAT	OR	ON	2005/07/20 08:27
L2	1342	totem adj pole	US-PGPUB; USPAT	OR	ON	2005/07/20 08:27
L3	1	1 and 2	US-PGPUB; USPAT	OR	ON	2005/07/20 08:27

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	X	X	US 20050088901 A1	20050428	14	CMOS isolation cell for embedded memory in power failure environments	365/226

	Current XRef	Retrieval Classif	Inventor	S	C	P	Image Doc. Displayed	PT
1			Day, Brian A. et al.				US 20050088901	